## Chrome Series DDR3

**1GB DIMMs and 3GB Triple Channel Kits** 



Our Chrome series is designed by hardware enthusiasts for maximum performance with the Core i7 CPU. These modules are hand tested in the latest Core i7 motherboards in triple channel configuration to guarantee phenomenal speed. Each Chrome series module is equipped with a chrome plated heatsink that is designed by our thermal engineers to wick away heat from the components more efficiently than traditional heatsinks, resulting in higher clock speeds. The Chrome series is built expressly for extreme overclockers and gaming enthusiasts.

**\*TALEN** 

## **Ordering Information**

WB160UA1G9	1GB DDR3-1600 CL9 DIMM
WB160UA1G8	1GB DDR3-1600 CL8 DIMM
WB160UA1G7	1GB DDR3-1600 CL7 DIMM
WB180UA1G8	1GB DDR3-1800 CL8 DIMM
WB200UA1G9	1GB DDR3-2000 CL9 DIMM
WB160UX3G9	3GB DDR3-1600 CL9 Triple Channel Kit
WB160UX3G8	3GB DDR3-1600 CL8 Triple Channel Kit
WB160UX3G7	3GB DDR3-1600 CL7 Triple Channel Kit
WB180UX3G8	3GB DDR3-1800 CL8 Triple Channel Kit
WB200UX3G9	3GB DDR3-2000 CL9 Triple Channel Kit

## **Module Features**

Chip Architecture	8 pcs 128Mx8 chip per DIMM		
Ranks	Single Rank		
Module Architecture	128M x 64		
Туре	Non-ECC Unbuffered		
Package	240-pin DDR3 DIMM		
Heatsink	Hyper-efficient Chrome heatsink		
Default Latency in SPD	9-9-9-27		
Warranty	Super Talent Lifetime Warranty		

## **Test Specs**

CPU	Core i7
Modules Installed	3 DIMMs only*
Vdimm	1.65V
Motherboard	Asus Striker Extreme II or equiv

	WB160	WB180	WB200
Clock Speed	800MHz	900MHz	1000MHz
DDR Speed	DDR3-1600	DDR3-1800	DDR3-2000
Data Rate	PC3-12800	PC3-14400	PC3-16000

Latencies	CAS - tRCD - tRP - tRAS
WB160UxxG9	9-9-928
WB160UxxG8	8-8-8-24
WB160UxxG7	7-7-724
WB180UxxG8	8-8-8-24
WB200UxxG9	9-9-928

Specifications subject to change without notice. You may achieve better or worse results depending on your other system components. \* All triple-channel overclock kits are tested with three modules installed; rated specs are not guaranteed for six modules in a system. © Copyright 2009, Super Talent Technology. Revised 02 Jul 09.

